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| FORM PTO-1449<br>(REV. 7-80)  | U.S. DEPARTMENT OF COMMERCE<br>PATENT AND TRADEMARK OFFICE | ATTY. DOCKET NO.<br>H-780-02 | SERIAL NO.    |
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| EXAMINER INITIAL | DOCUMENT     | DATE     | NAME              | CLASS | SUBCLASS | FILING DATE<br>(If Appropriate) |
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| JT               | AA 5,621,691 | 04/15/97 | Park              |       |          |                                 |
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| JT               | AC 5,430,679 | 07/04/95 | Hiltebeitel et al |       |          |                                 |
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| JT               | AE 4,310,901 | 01/1982  | Harding et al     |       |          |                                 |
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| JT               | AI 5,841,711 | 11/1998  | Watanabe          |       |          |                                 |
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|----|-------------|----------|---------------------------|-------|----------|--|
| JT | AL 8-77791  | 03/22/96 | Japanese Laid-Open Patent |       |          | <input type="checkbox"/> <input type="checkbox"/>                      |
| JT | AM 2-192100 | 07/27/90 | Japanese Laid-Open Patent |       |          | <input type="checkbox"/> <input type="checkbox"/>                      |
| JT | AN 4-274096 | 09/30/92 | Japanese Laid-Open Patent |       |          | <input type="checkbox"/> <input type="checkbox"/>                      |
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| JT | AR | IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol. 28, No. 11, Nov. 1993, "256-Mb               |
|    |    | DRAM Circuit Technologies for File Applications", G. Kitsukawa et al,<br>pp. 1105-1113. |
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| EXAMINER<br>Joseph Torres   | Digitally signed by Joseph Torres<br>On: 04/27/2006 at 16:26:19 -04'00'<br>Date: 2006.04.27 16:26:19 -04'00' | DATE CONSIDERED<br>4/27/2006 |
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